

**Form 1449 (Modified)****Information Disclosure  
Statement By Applicant**

(Use Several Sheets if Necessary)

Atty. Docket No. EMB1P004 Application No.: 09/687,011

Applicant:

Saul et al.

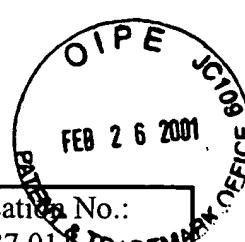
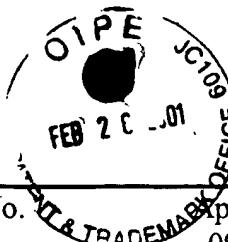
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**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	6,081,903	06/27/00	Vorbach et al.	713	400	10/08/97
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
AB	L	00/38087	29.06.00	WO				
	M	97/32259	04.09.97	WO				
↓	N	0772140 A1	07.05.97	EP				
↓	O	97/13209	10.04.97	WO				
↓	P	94/10627	11.05.94	WO				

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	R	Blickle, Tobias et al., "System-Level Synthesis Using Evolutionary Algorithms", Computer Engineering and Communication Networks Lab, April 16, 1996.
↓	S	Edwards, M.D. et al., "Software acceleration using programmable hardware devices," Computers and Digital Techniques, January 1996.
↓	T	Lechner, Eric et al., "The Java Environment for Reconfigurable Computing"
Examiner		Date Considered 4/23/04

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.